

LECTURERS IN GOVERNMENT POLYTECHNIC COLLEGES (ENGINEERING AND NON-ENGINEERING) IN A.P TECHNICAL EDUCATION SERVICE. - NOTIFICATION
NO.23/2018

COMPUTER ENGINEERING- 13TH MAR 2020 – S2 – REVISED KEY

Question Number : 9 Question Id : 231098909

A 64-bit full adder is designed using half-adders, where each 1-bit full adder module has the same structure. If each half adder is formed using 2-input NAND gates only, then estimate the total number of 2-input NAND gates required for such a 64-bit full adder.

Answer: Deleted

Question Number : 20 Question Id : 231098920

Consider five vertices A, B, C, D and E in a Graph G with undirected edges connecting A and B, B and C, C and D; and D and A. What is the chromatic number of the graph G?

Answer:

2

Question Number : 57 Question Id : 231098957

When a base class is privately inherited by a derived class, the public members of the base class can only be accessed by the _____ of the derived class.

Answer: Deleted

Question Number : 58 Question Id : 231098958

When the base class is publicly inherited, the public members of the base class become _____ of the derived class.

Answer: Deleted

Question Number : 63 Question Id : 231098963

Consider the following C function:

```
int mult(int a, int b)
{
    int x = a * b;
    return !a || x/a == b;
}
```

Select the correct option describing the use of the function.

Answer: Deleted

Question Number : 71 Question Id : 231098971

The time complexity of Kruskal's minimum spanning tree algorithm is:

Answer: Deleted

Question Number : 93 Question Id : 231098993

Which of the following statements is not true about threads?

Answer: Deleted

Question Number : 135 Question Id : 2310981035

The False Positive Rate (FPR) is defined as:

Answer: Deleted